

ABSTRACT OF DISCLOSURE

A flat panel display comprises a system and a display module. The system comprises an image processing part for deciding a timing format of an image data and generating a control signal for the image data, an encoder for encoding the data and the control signal output from the image processing part in an RSDS specification, and a power output part for outputting a constant-voltage. The display module includes a column driver integrated circuit and a scan driver integrated circuit, and the decoding of the encoded image data and the control signal are performed at a decoder of the column driver integrated circuit and the scan driver integrated circuit

40
39
38
37
36
35
34
33
32
31
30
29
28
27
26
25
24
23
22
21
20
19
18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1